

Fig. 1

Fig.3

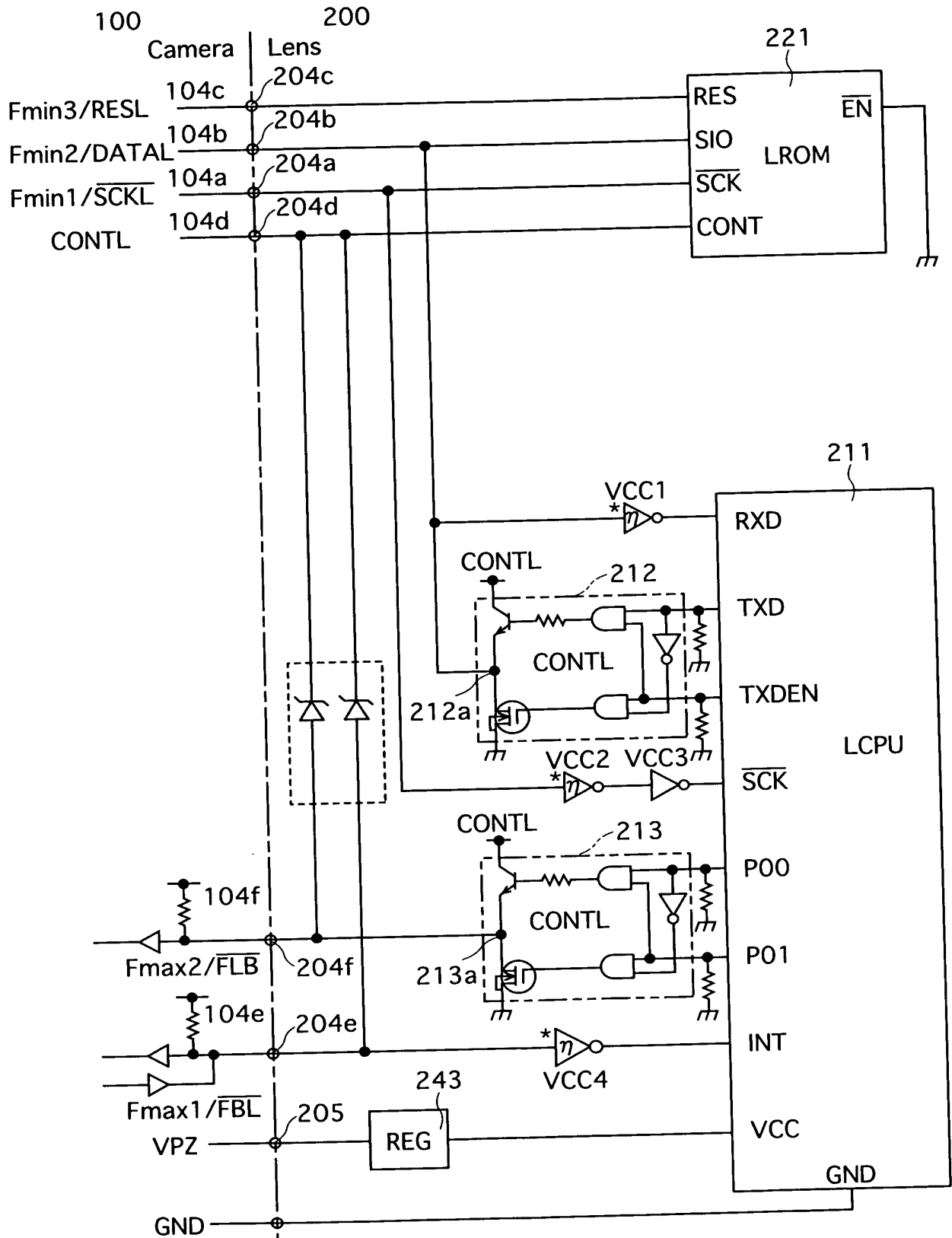


Fig.4

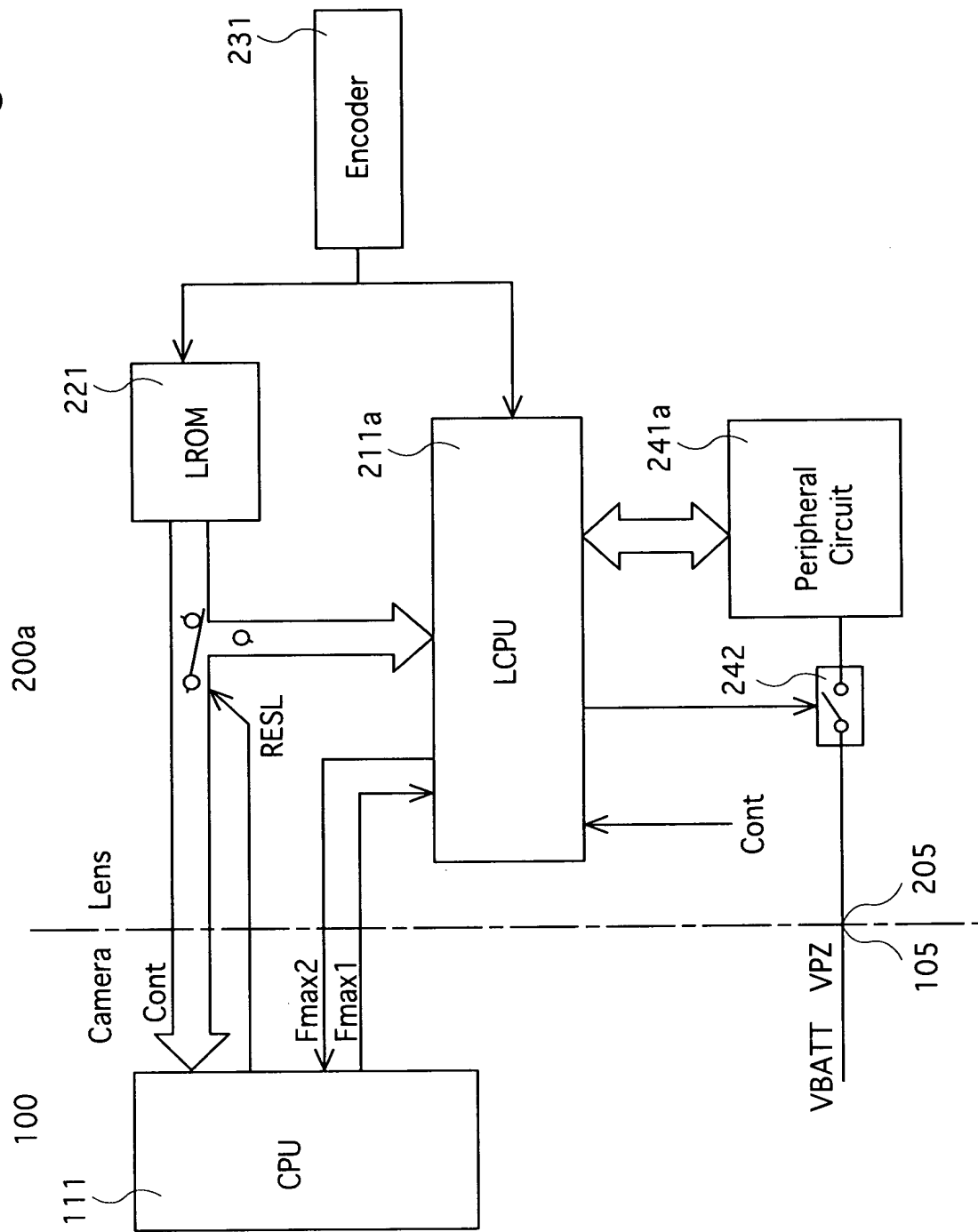


Fig.5

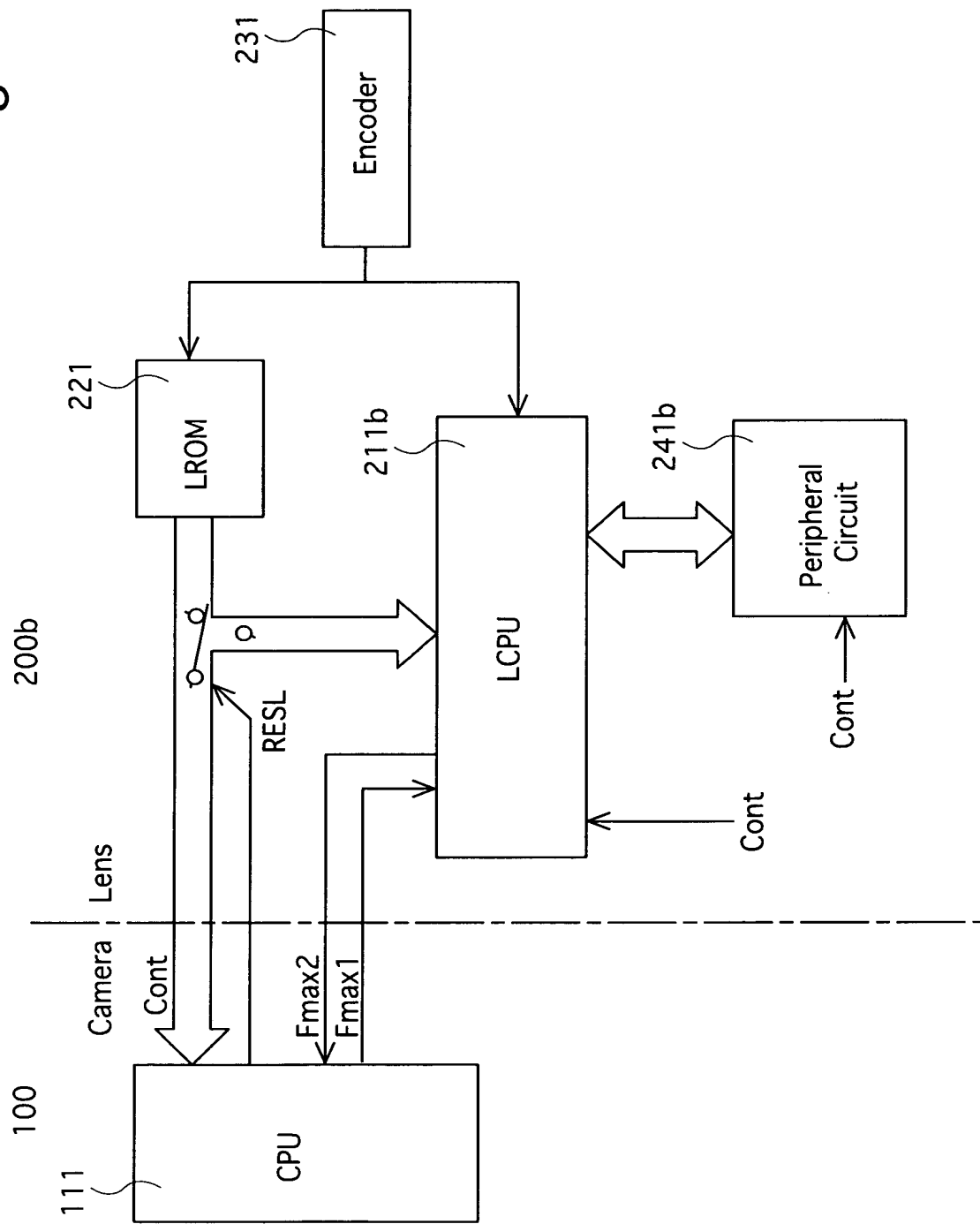
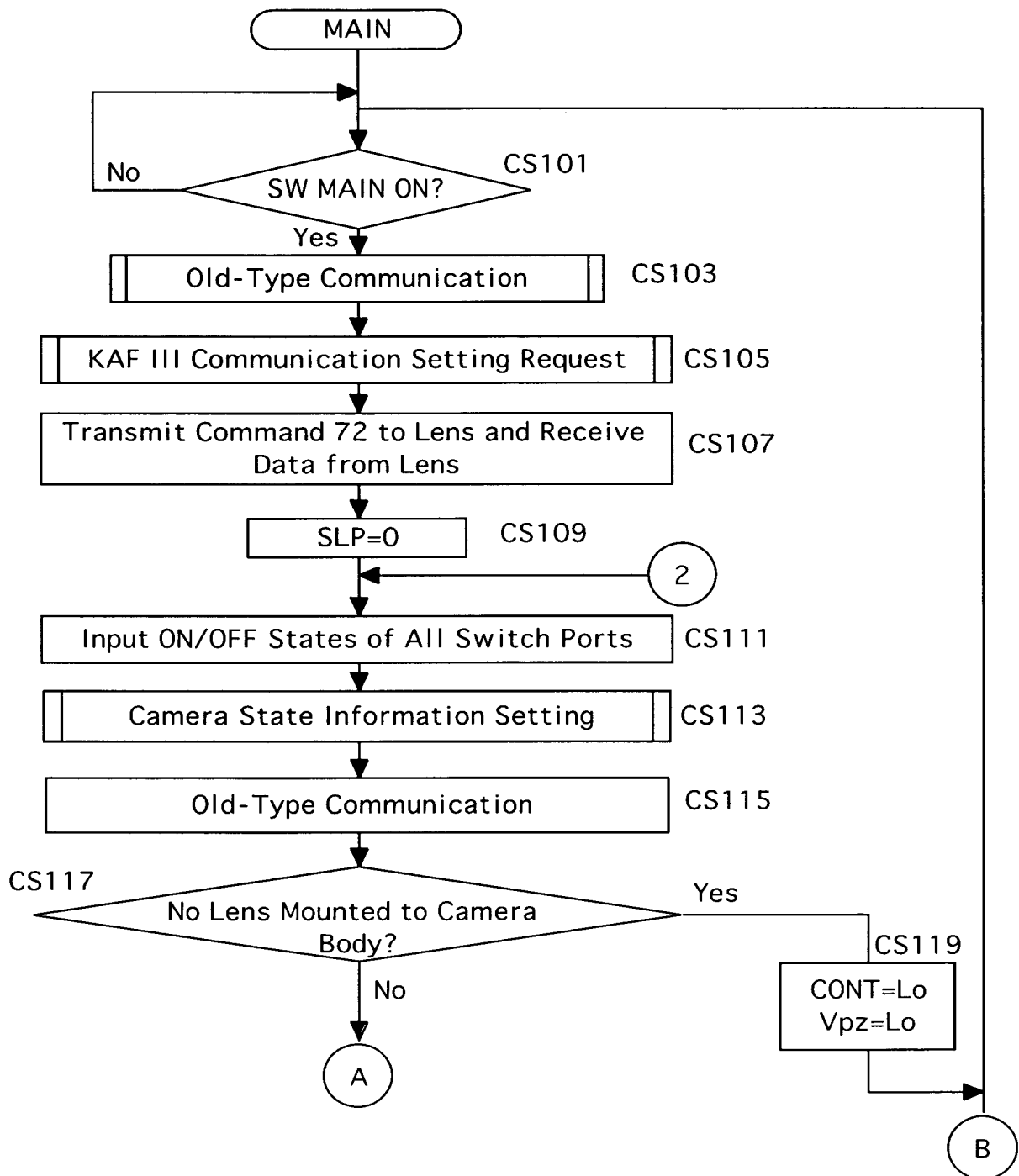


Fig. 6A



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Fig. 6B

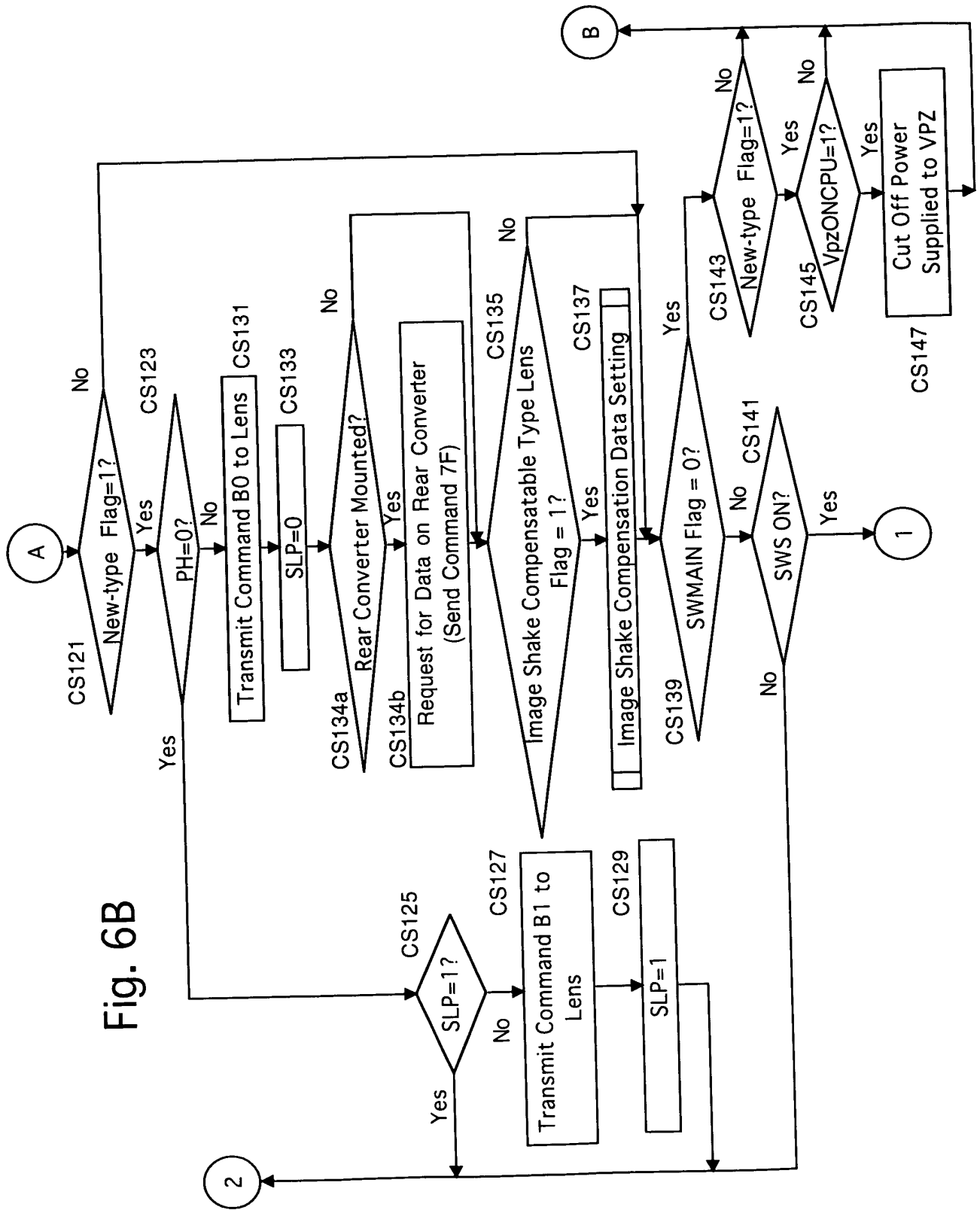
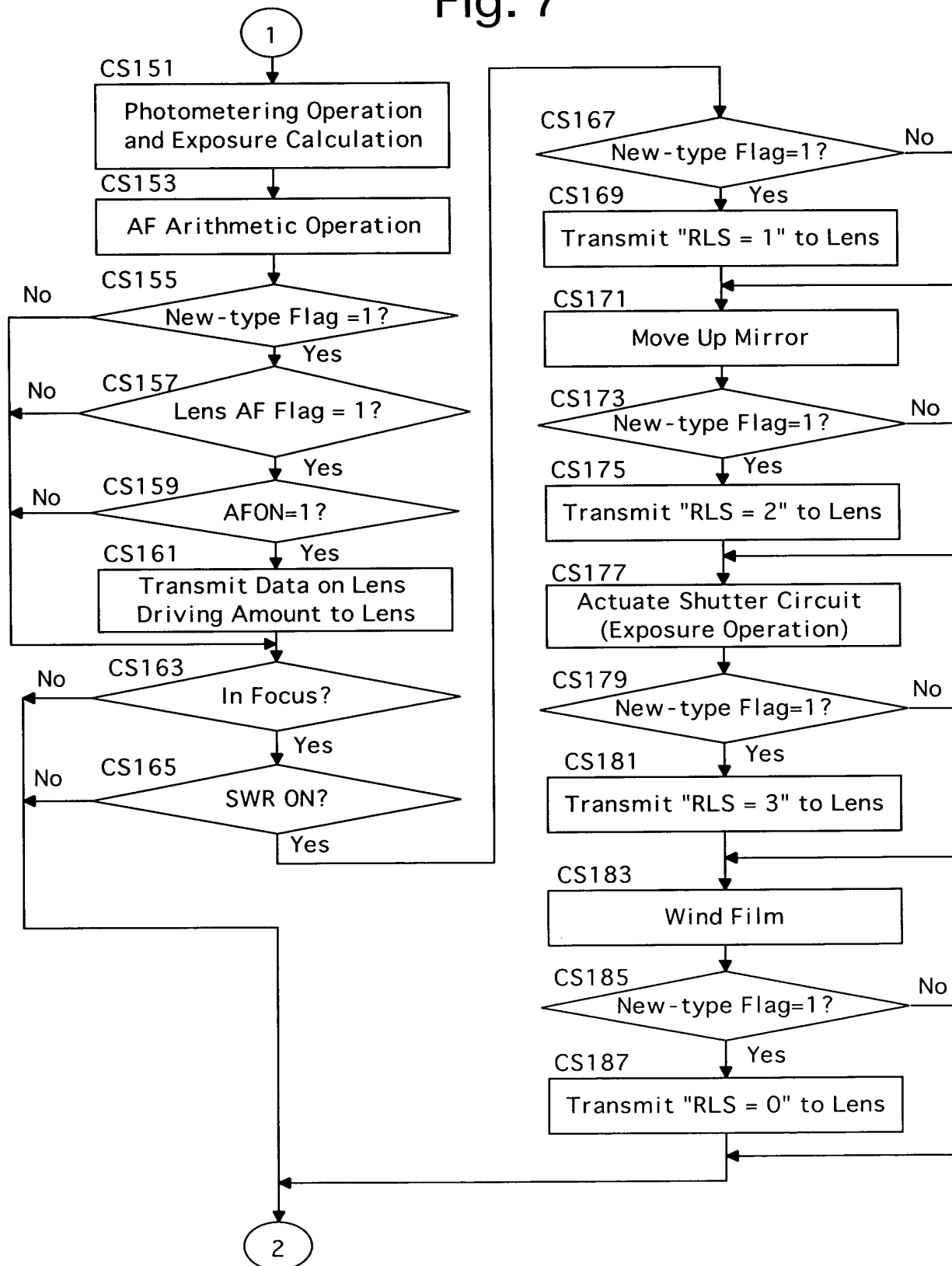


Fig. 7



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Fig. 8A

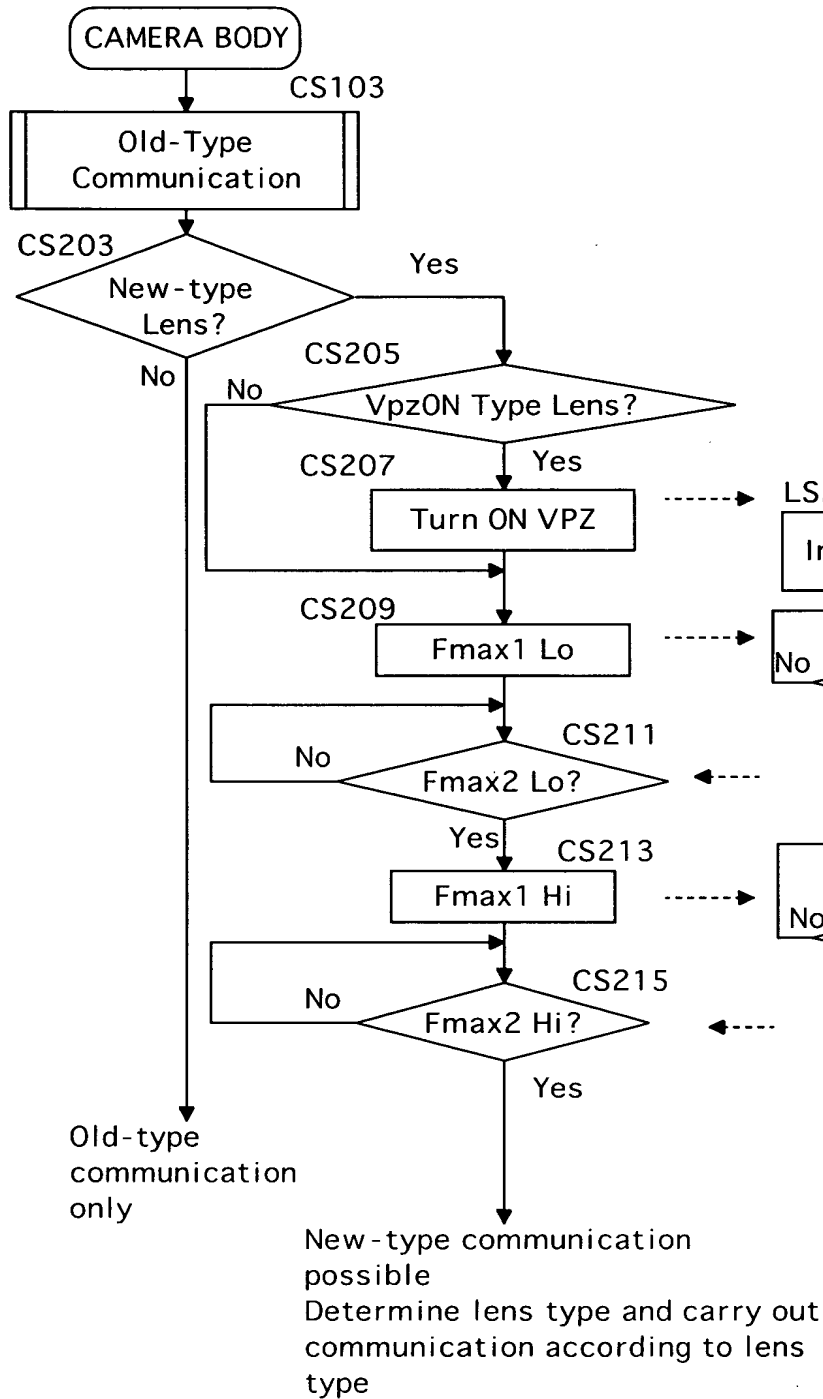


Fig.8B

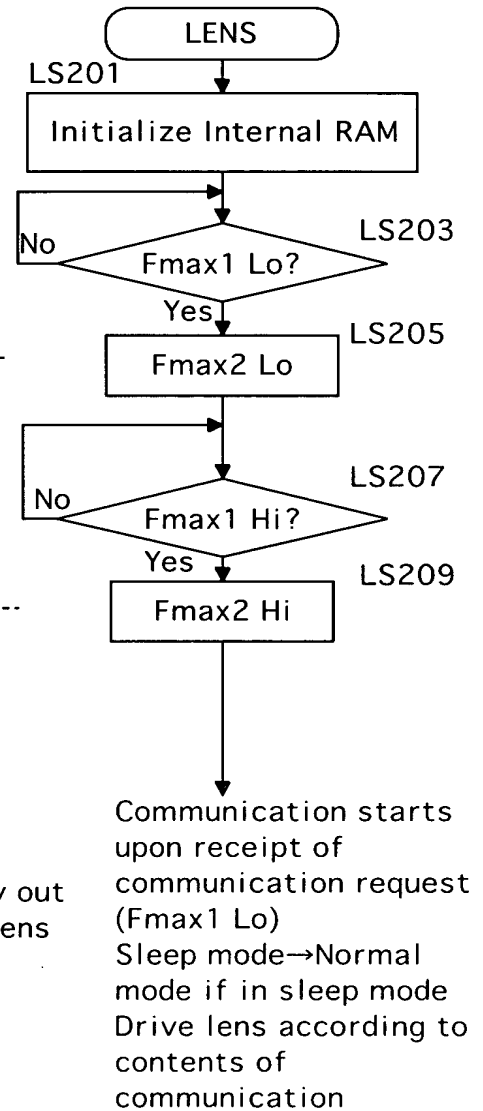
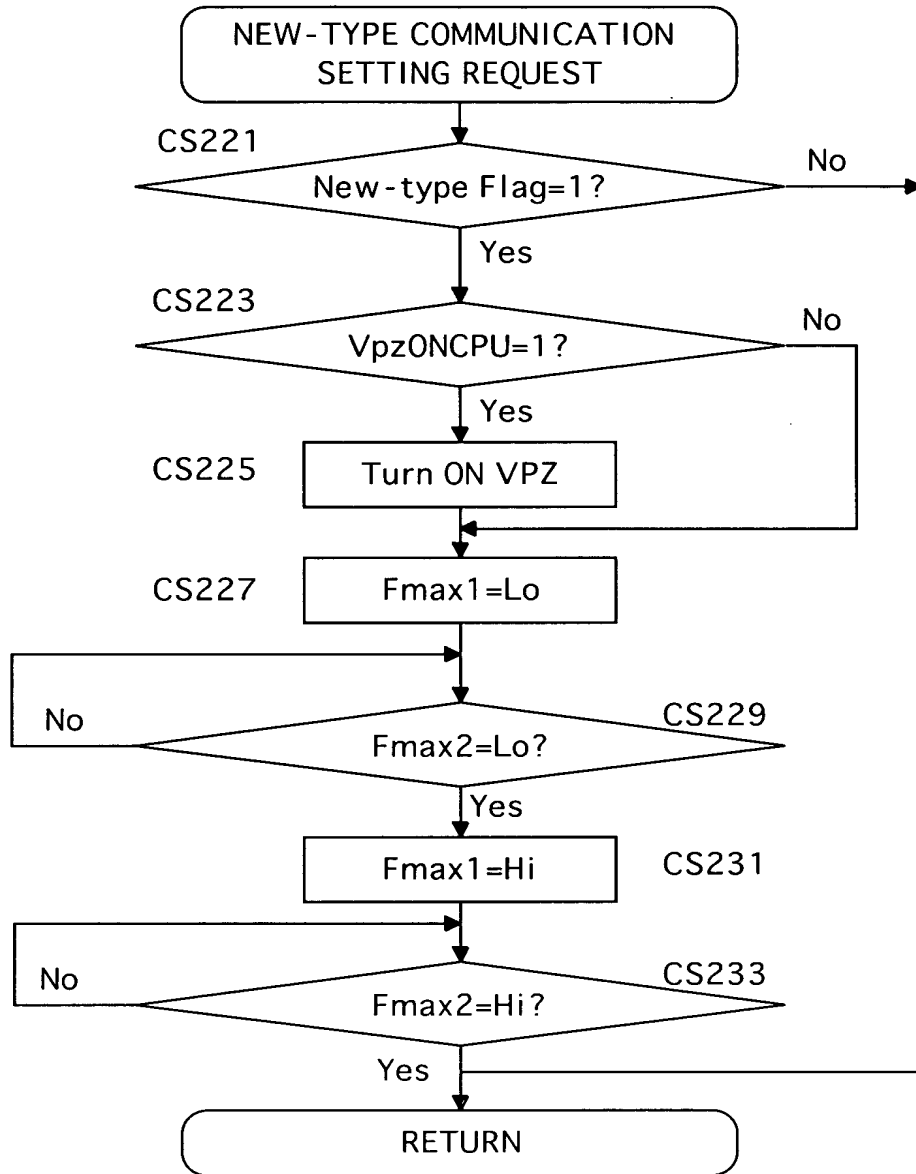
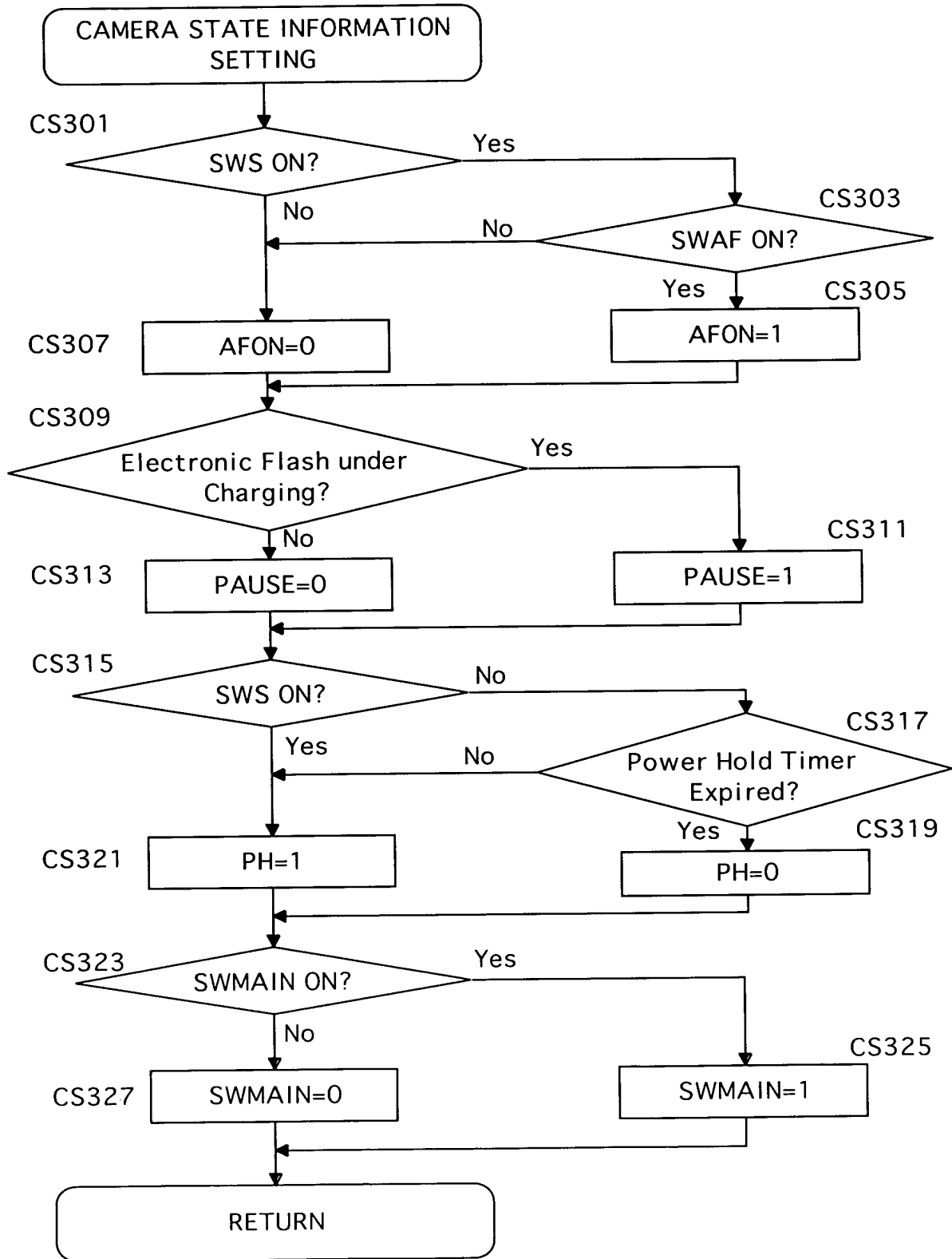


Fig.9



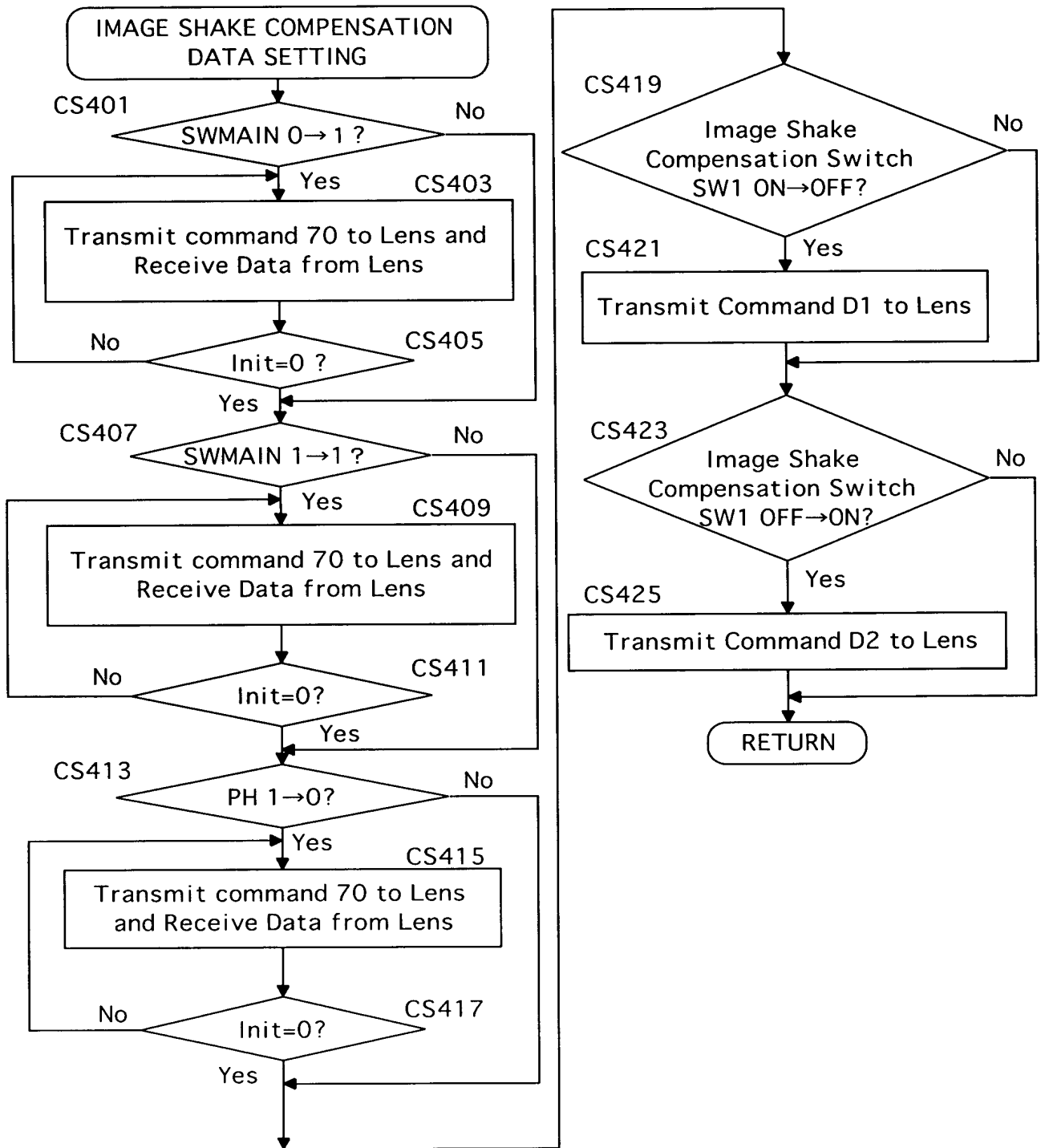
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Fig.10



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Fig.11



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Fig. 12B

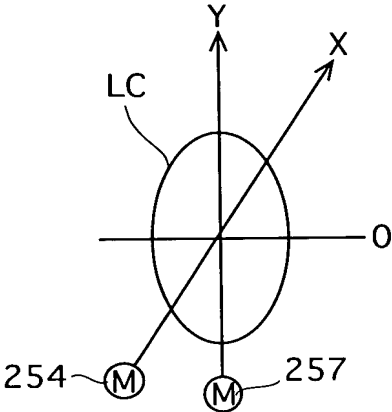


Fig.13

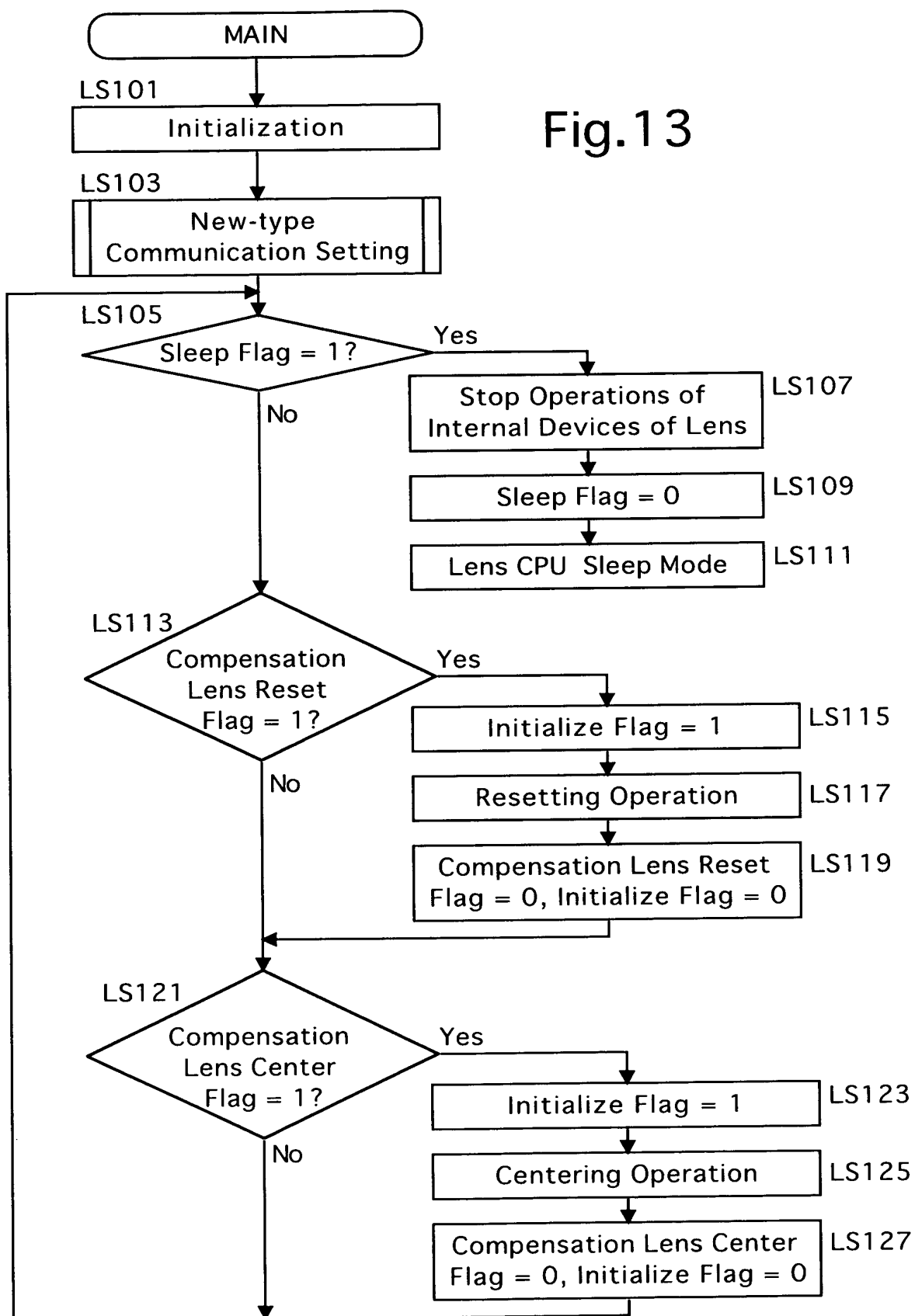
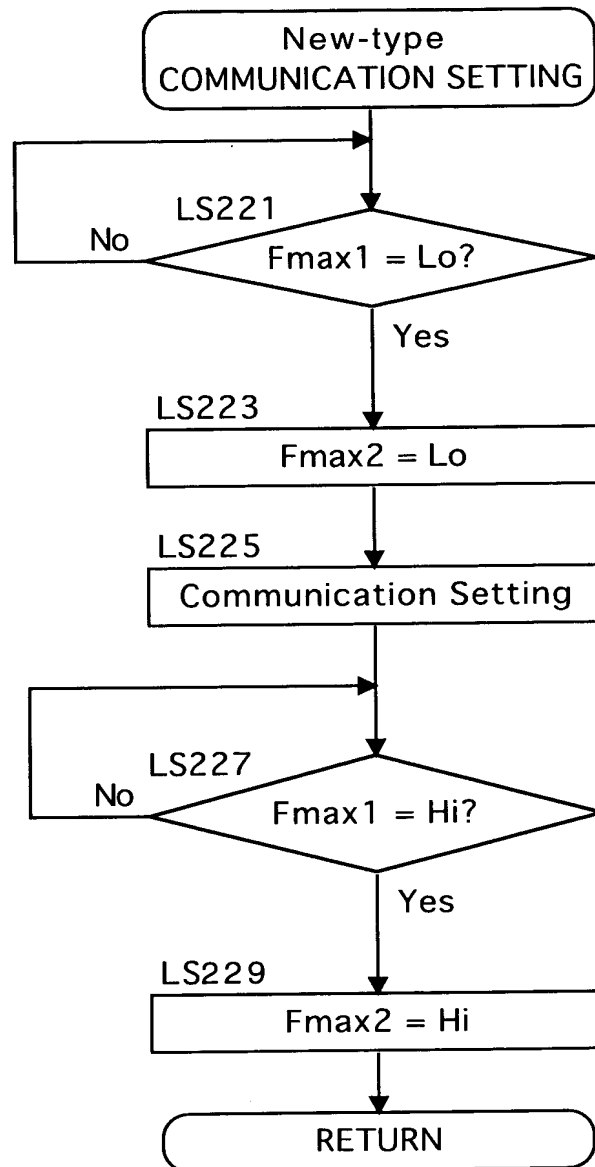


Fig.1 4



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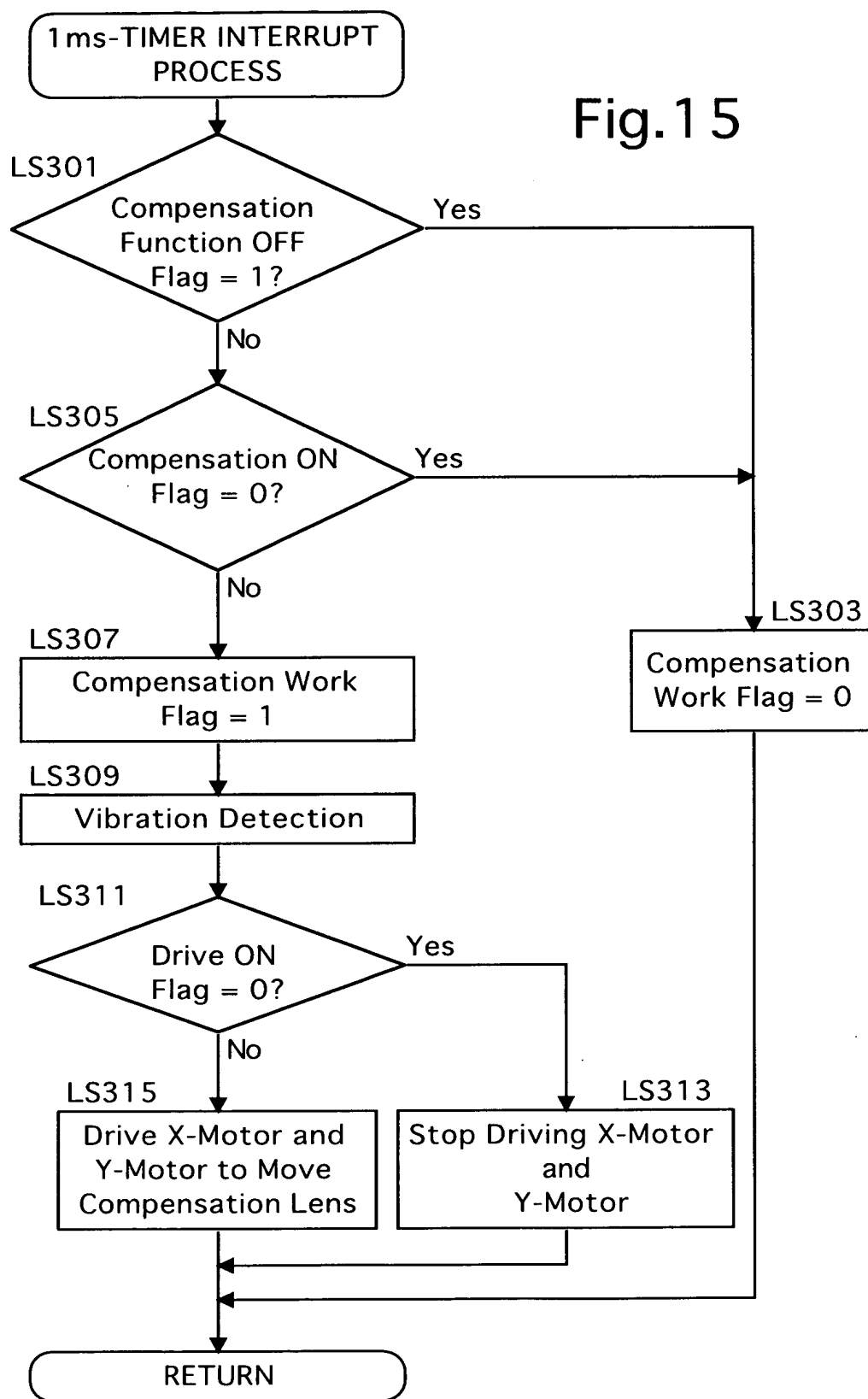
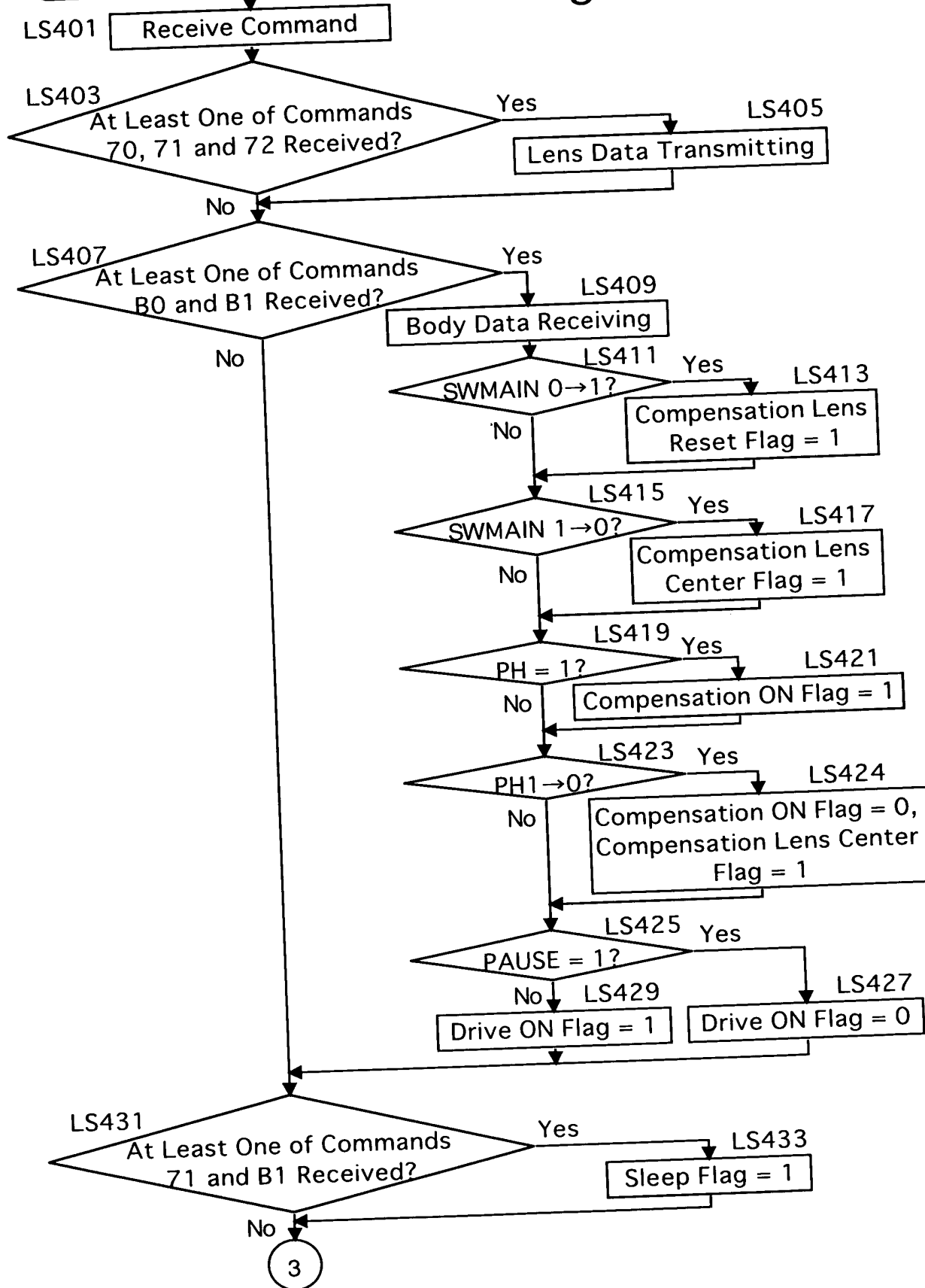


Fig.1 5

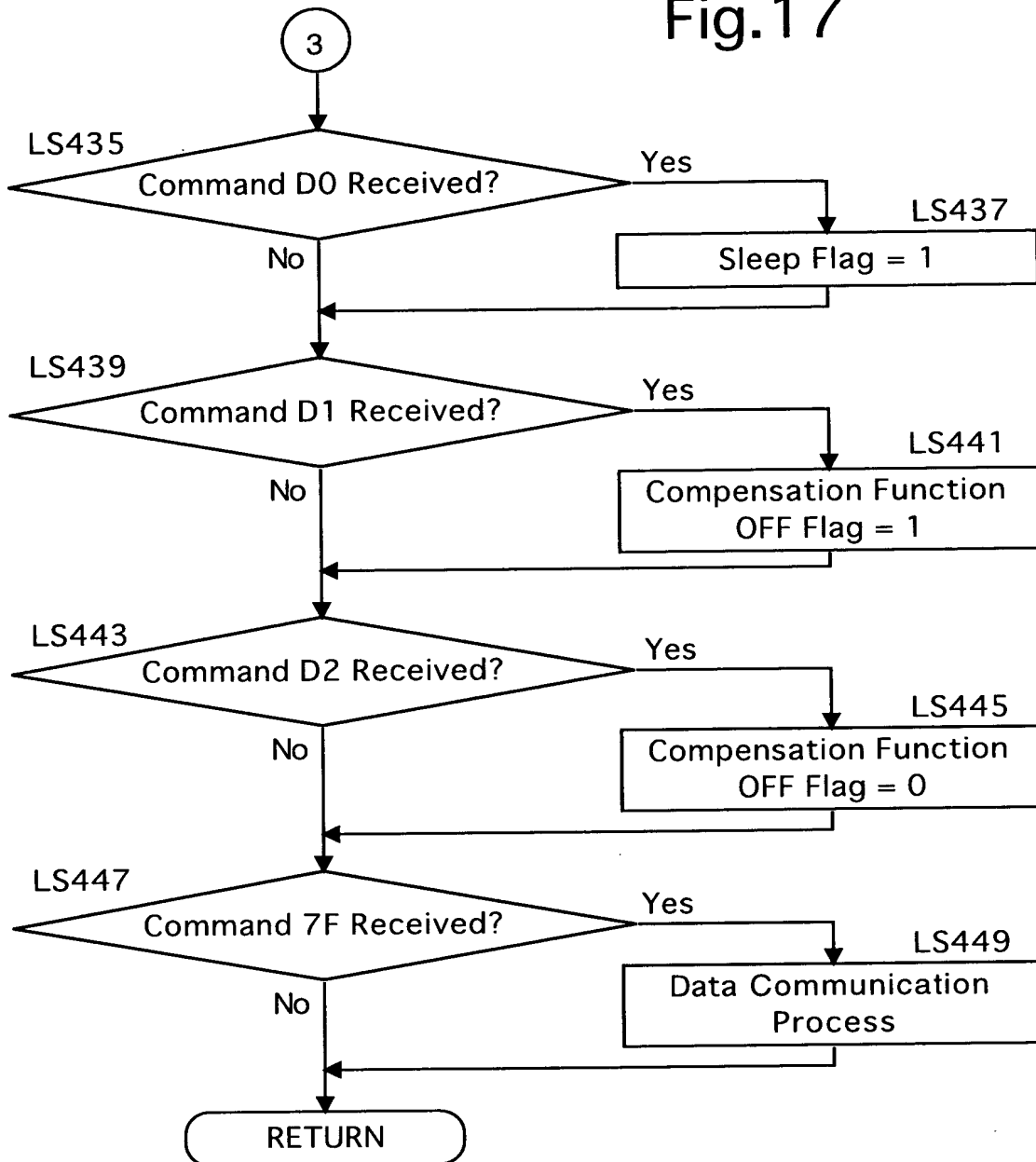
INVERSE-INT INTERRUPT PROCESS

Fig.16



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Fig.17



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202203180001

Fig. 18

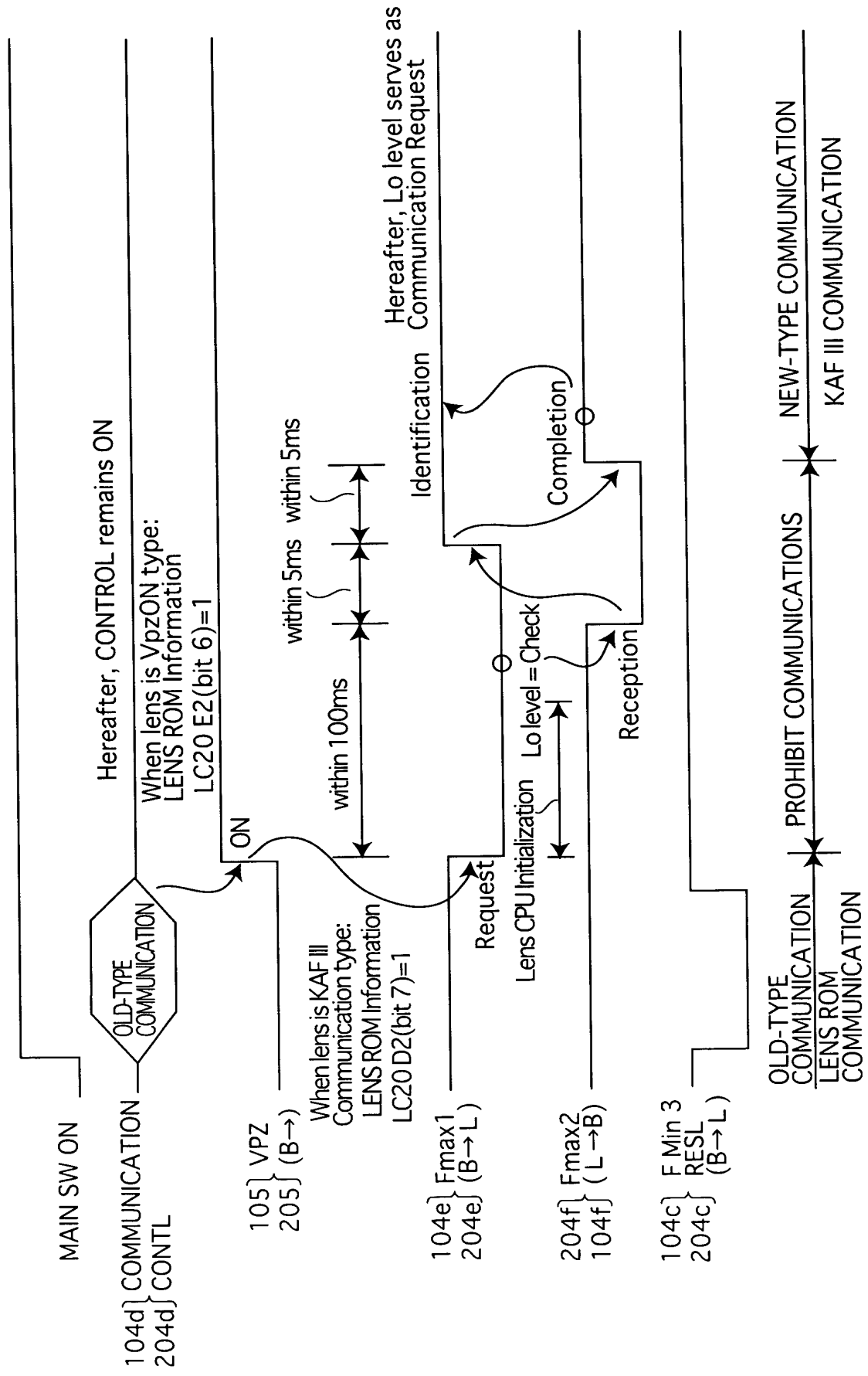


Fig. 19A

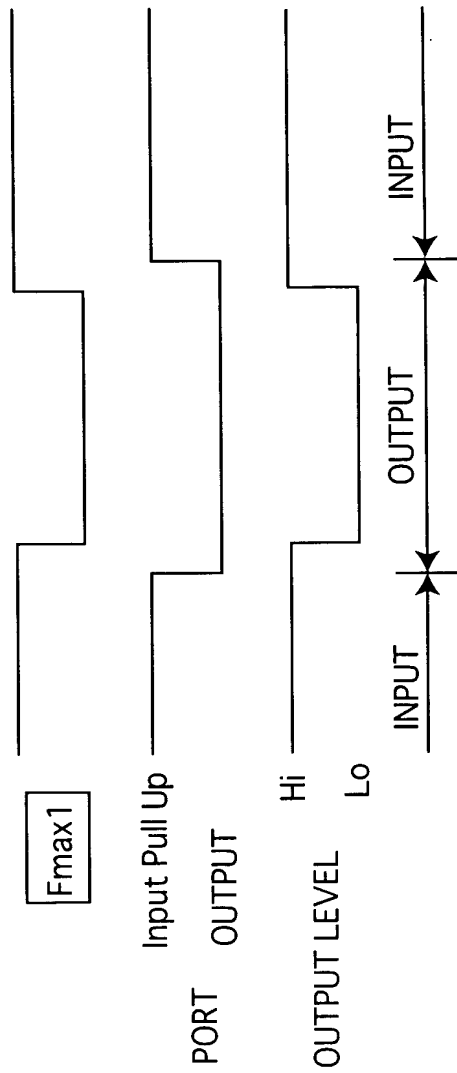


Fig. 19B

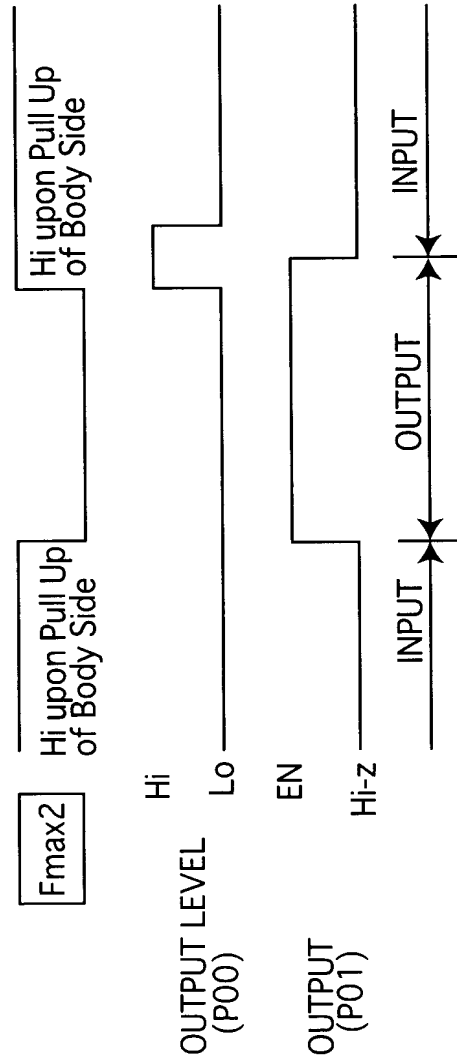


Fig. 20

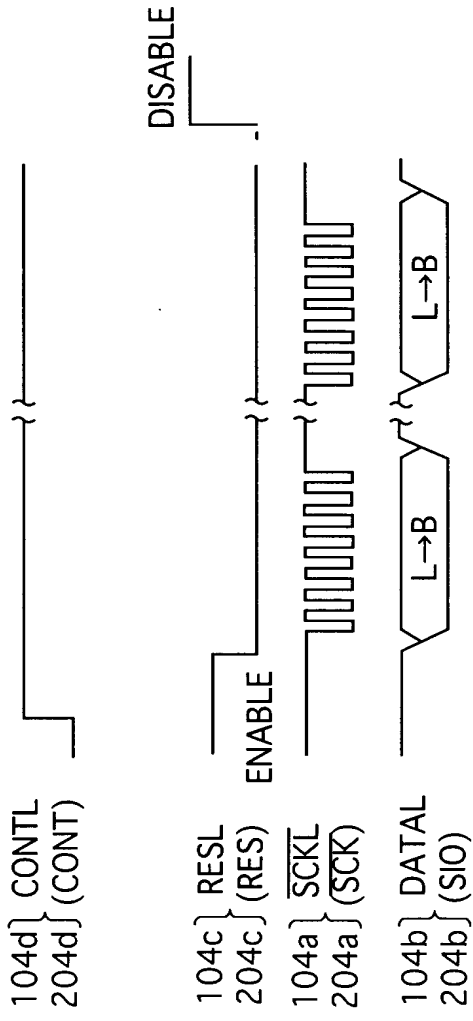
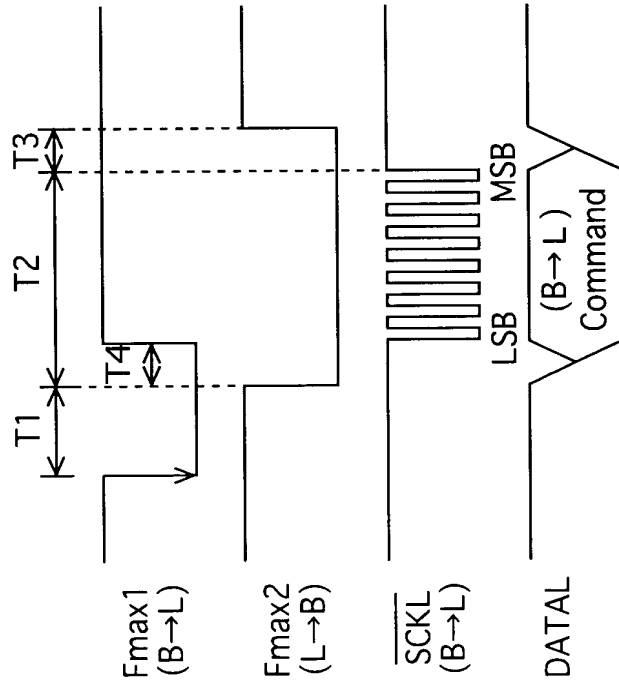


Fig.21A



- T1: within 50ms
- T2: within 5ms
- T3: within 5ms
- T4: within 5ms
- T5: within 5ms
- T6: within 5ms
- T7: within 5ms
- T8: within 5ms
- T9: within 5ms

Fig.21B

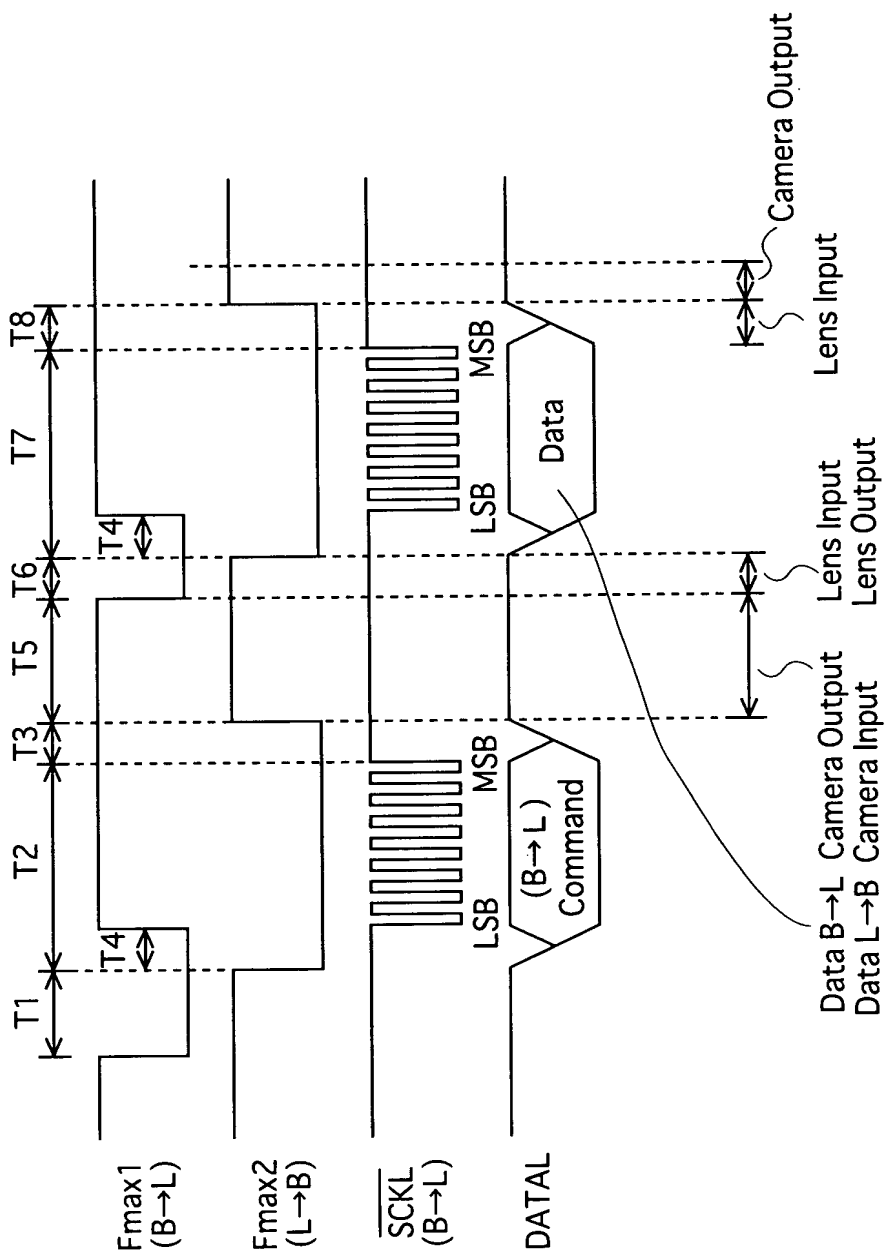
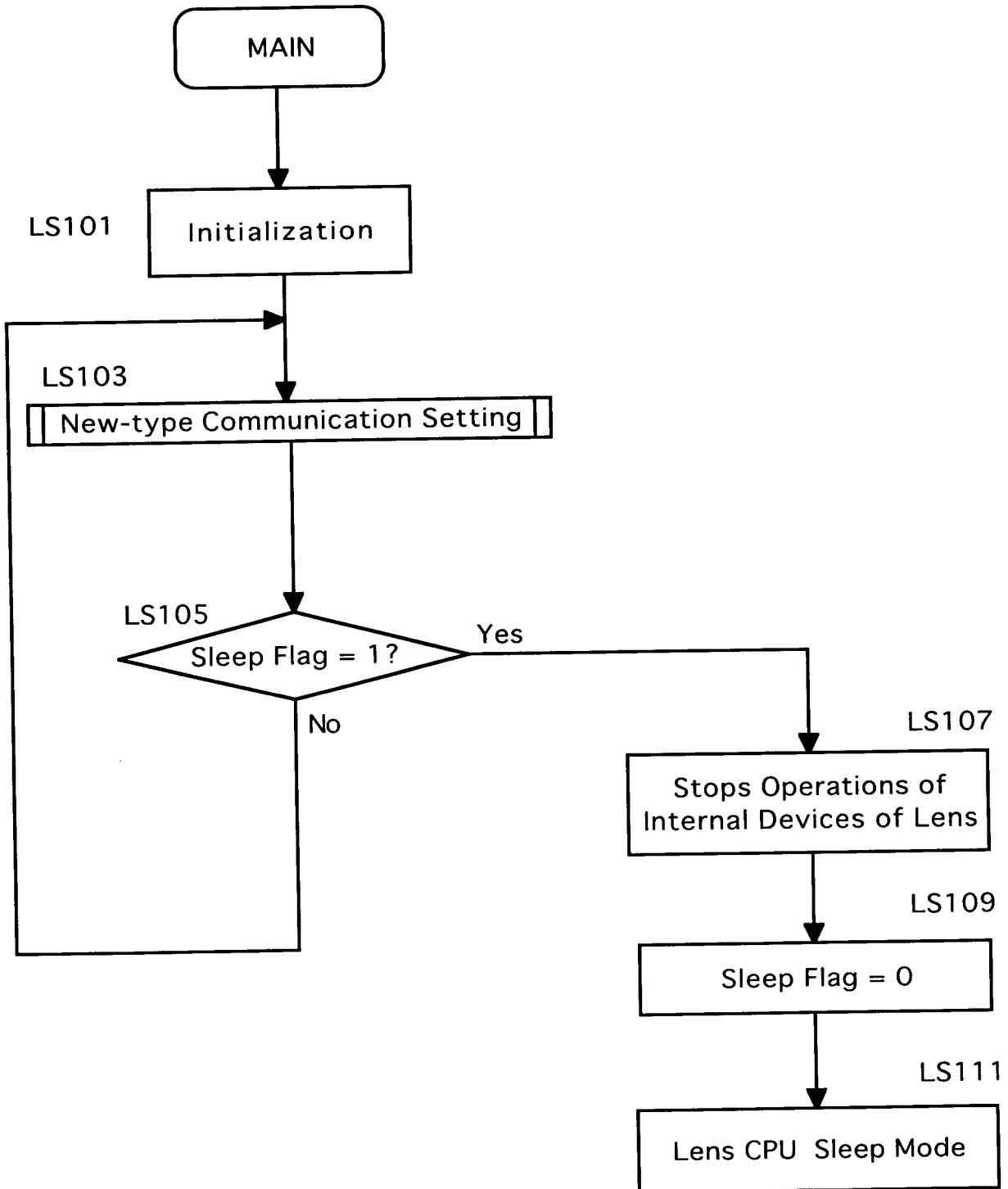
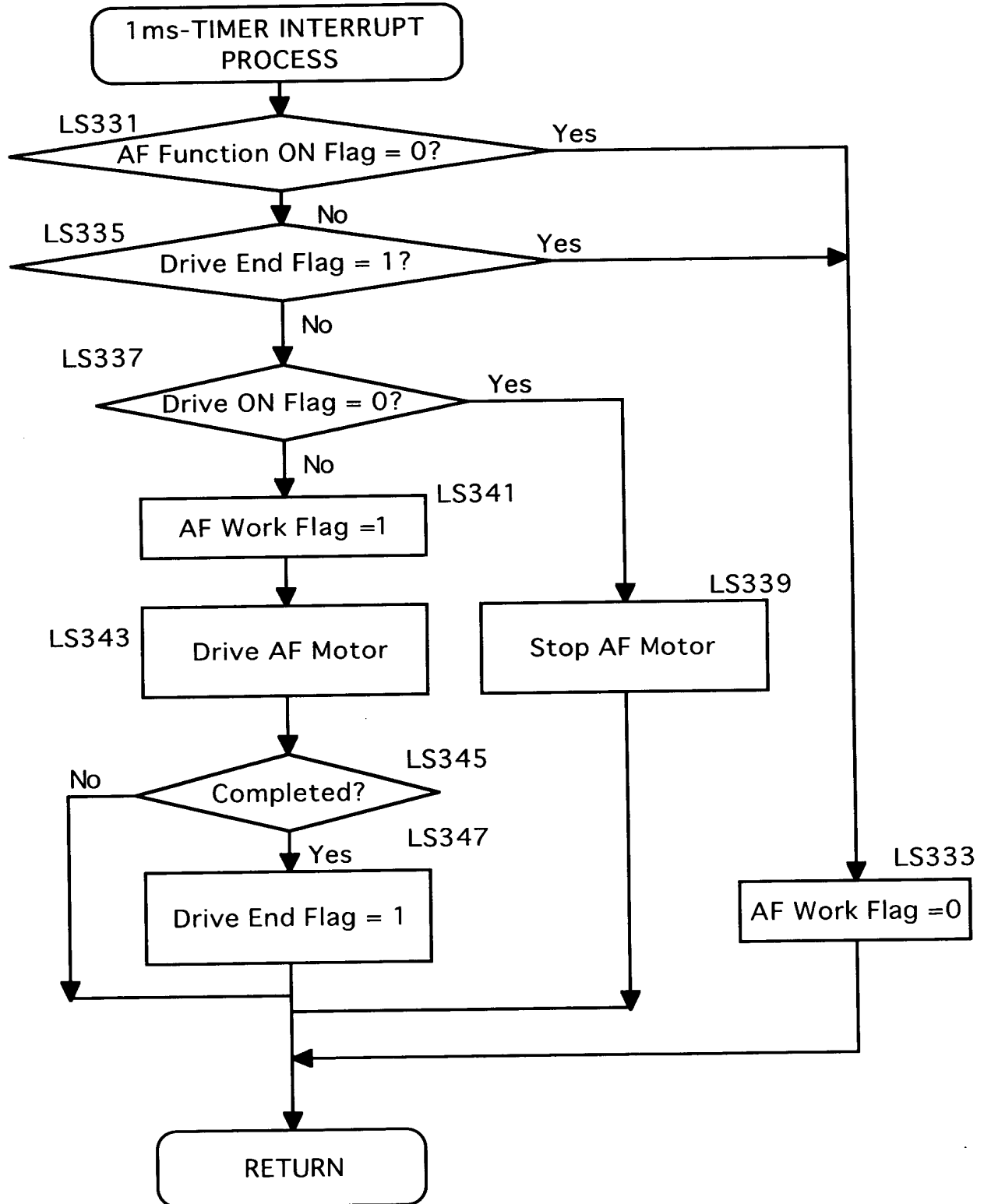


Fig. 23



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Fig. 24



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INVERSE INT INTERRUPT PROCESS

Fig. 25

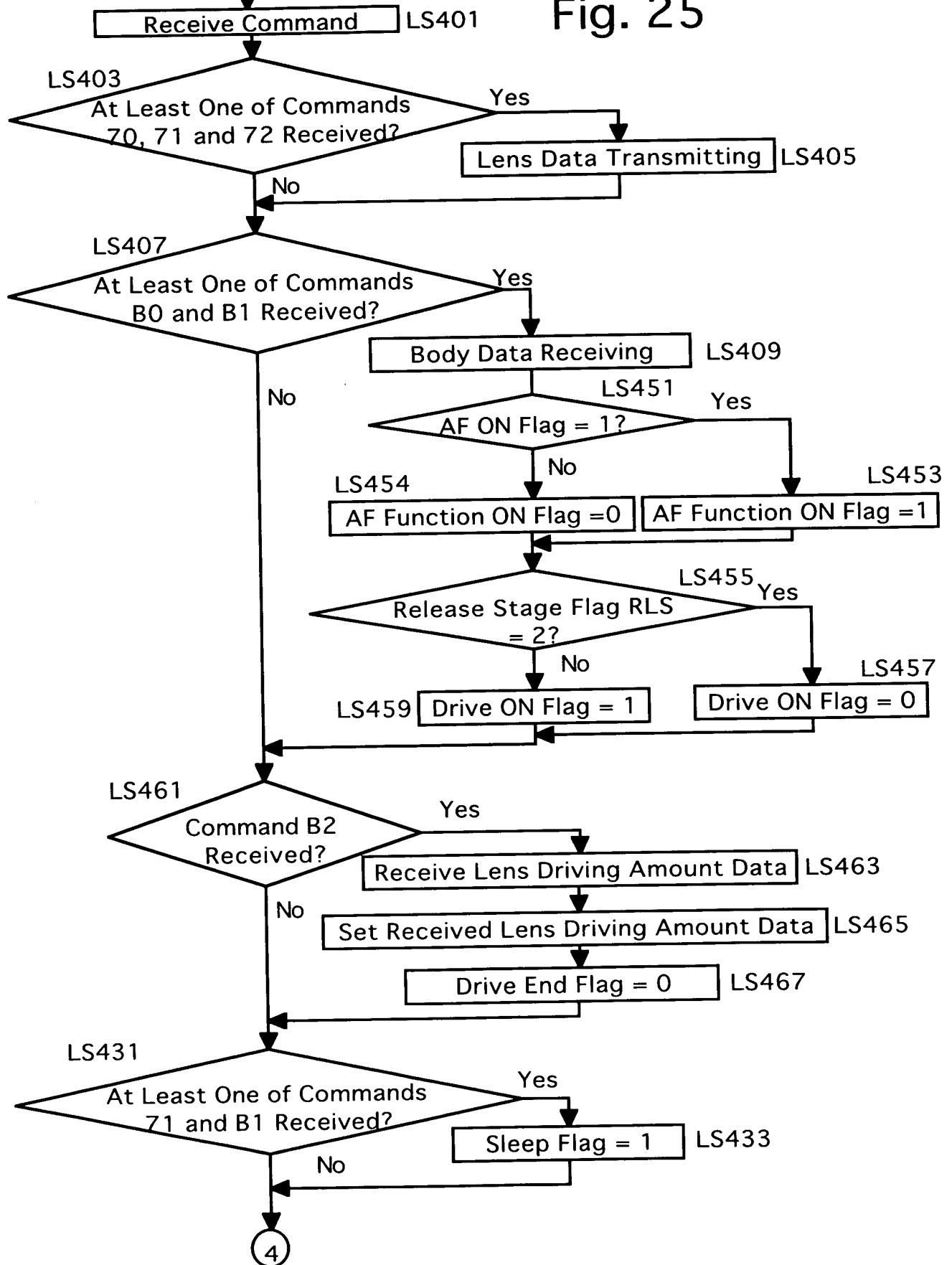
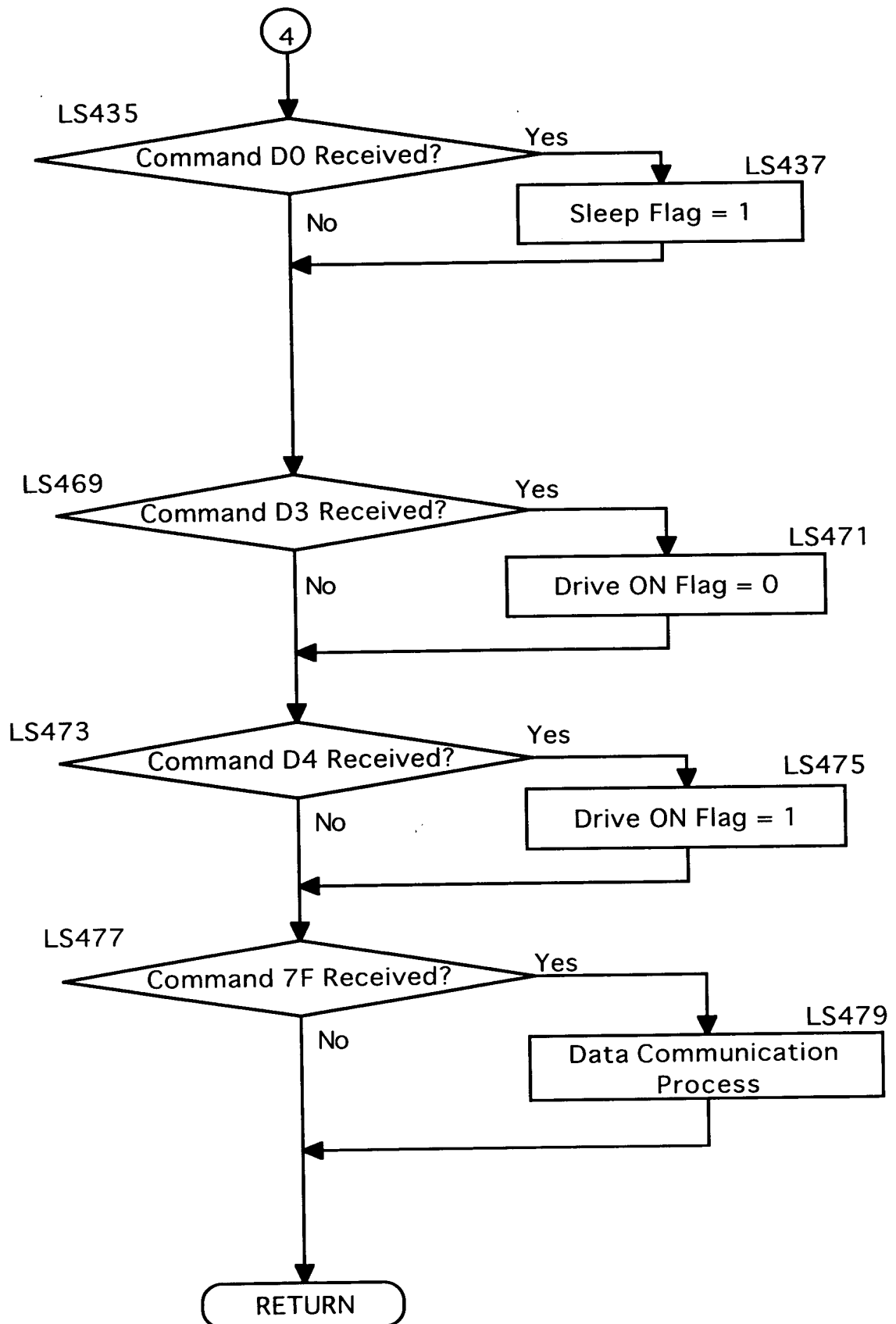


Fig. 26



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Fig.27

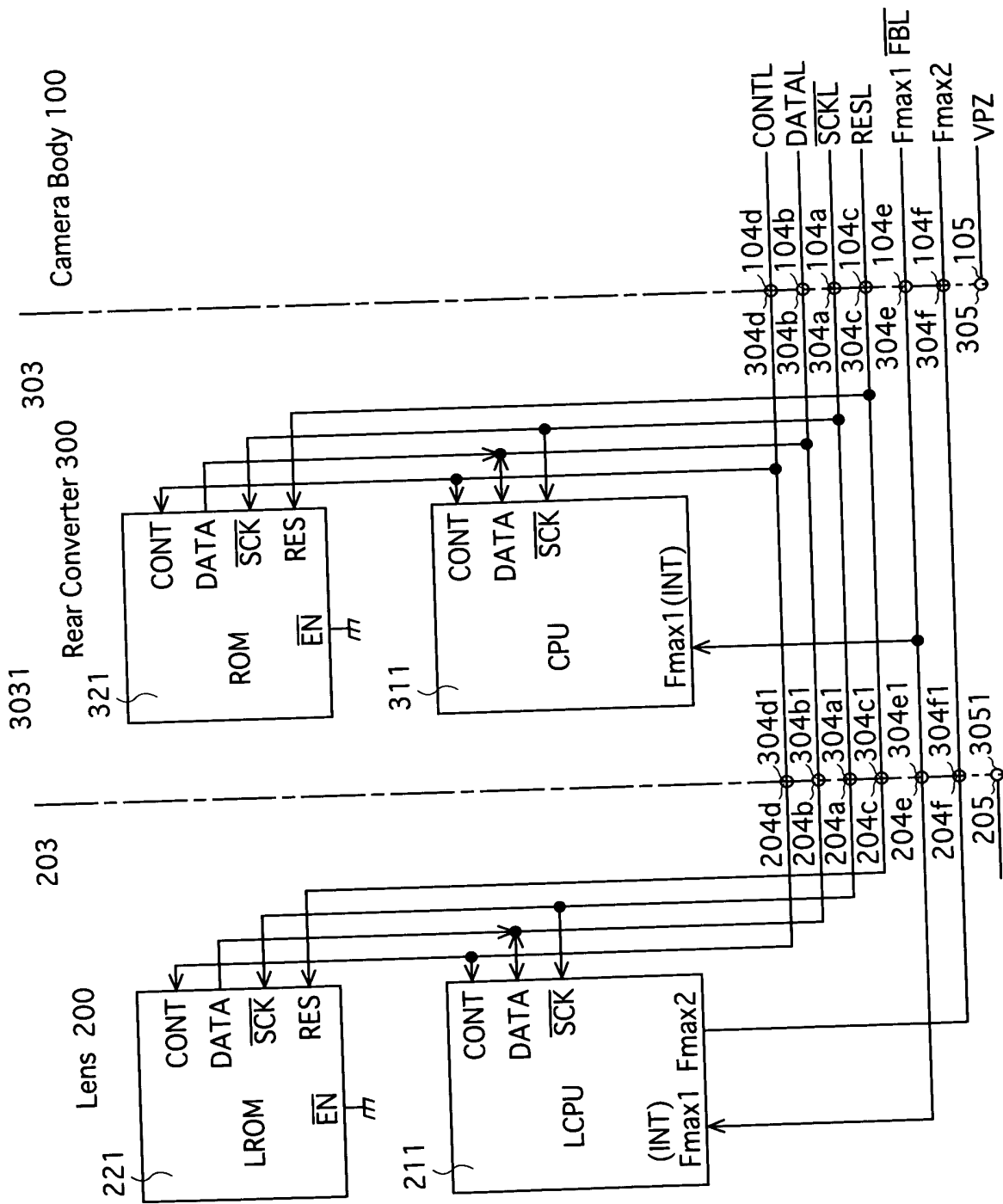


Fig. 28

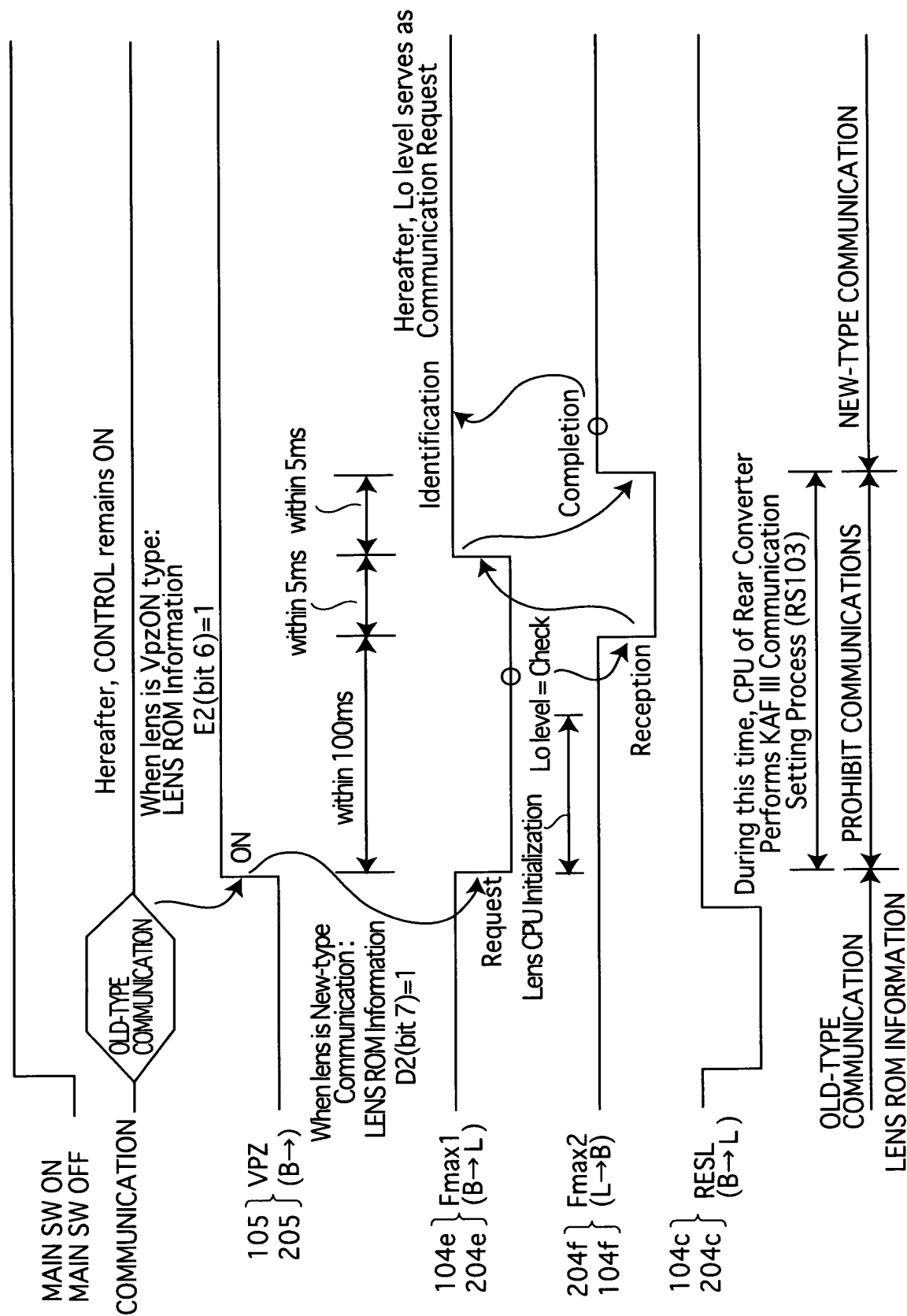
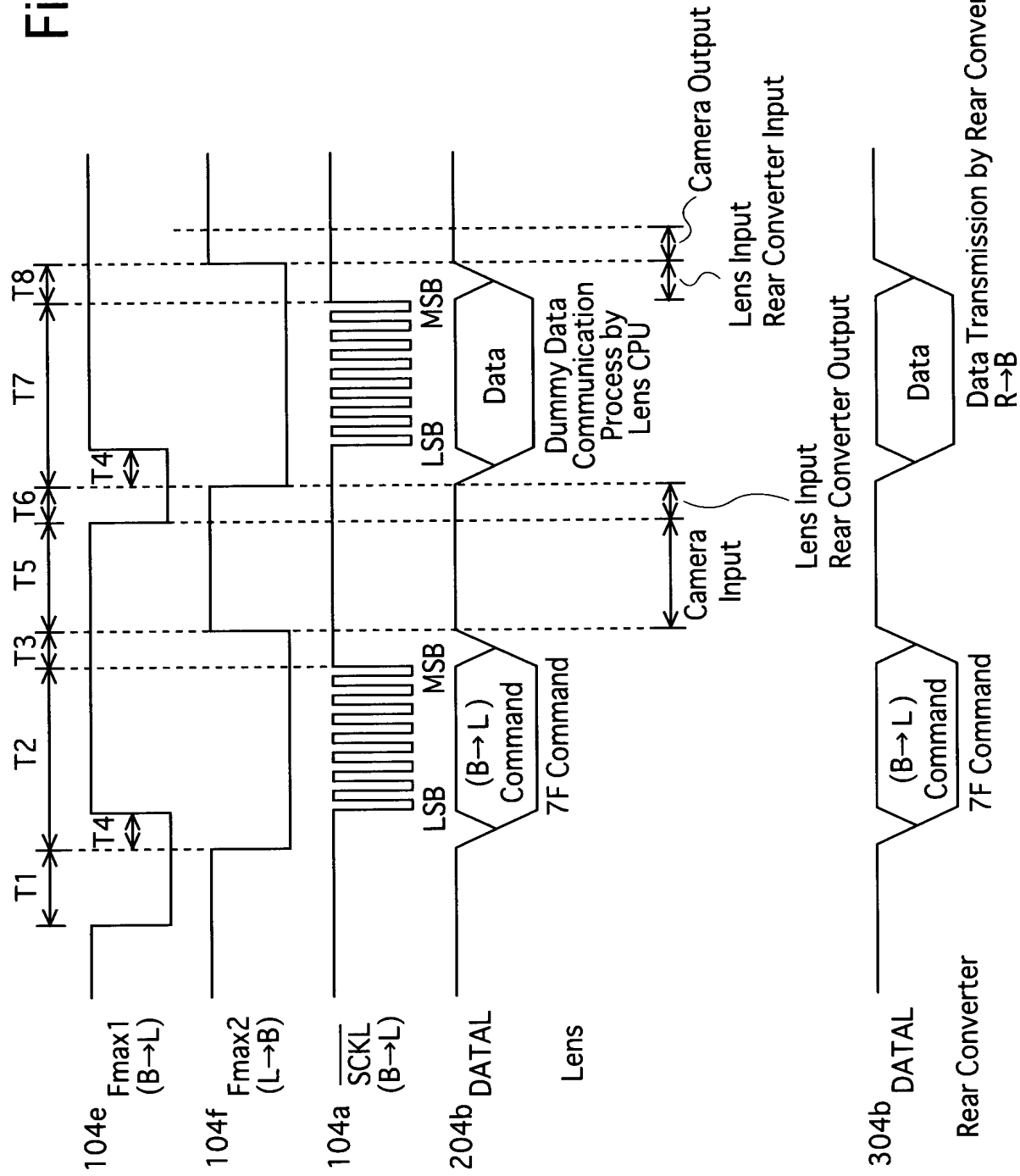


Fig.29



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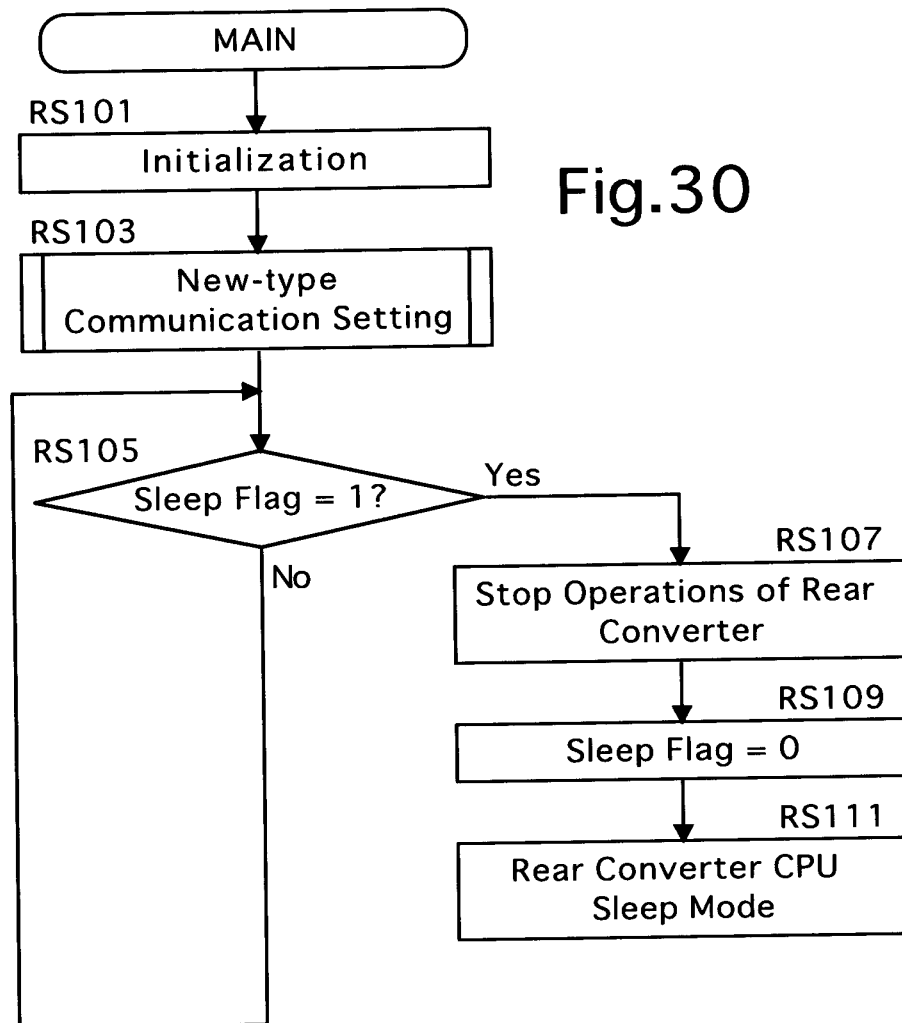
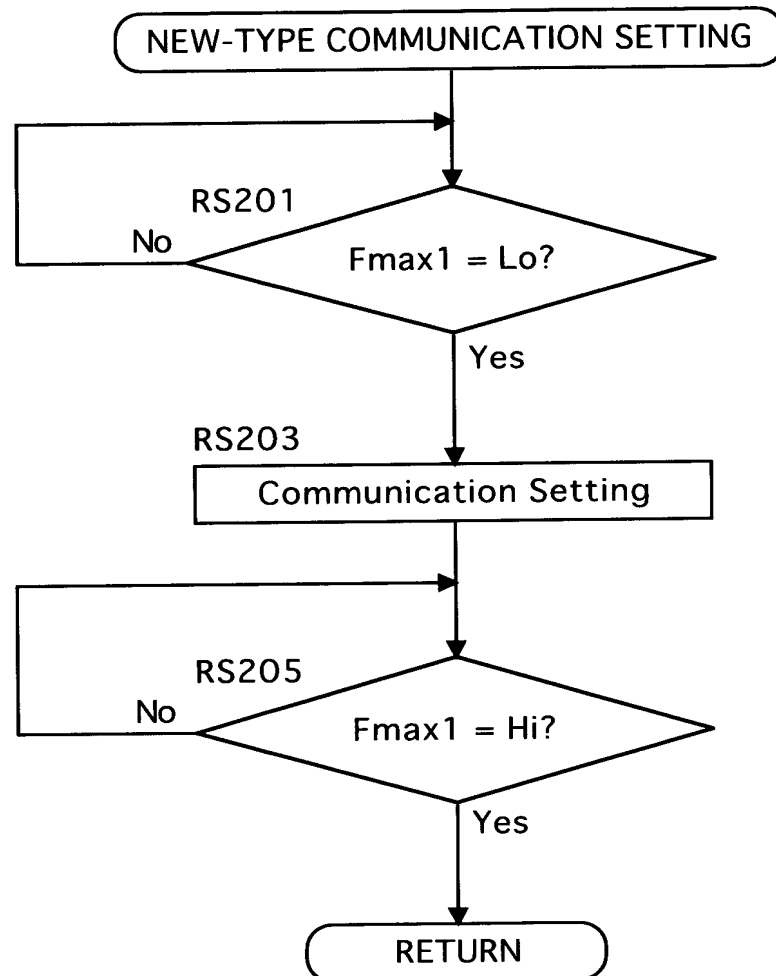


Fig.31



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